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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/020,628	
	Filing Date	12/14/2001	
	First Named Inventor	VISSER, Timothy Calvin	
	Group Art Unit	2185	
	Examiner Name	Not Assigned	
Total Number of Pages in This Submission		Attorney Docket Number	Smiths P177US

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual Name	VARNUM, RIDDERING, SCHMIDT & HOWLETT LLP PETER VISSERMAN		
Signature			
Date	6/24/02		

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: _____			
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PATENT

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: VISSER, Timothy Calvin
For: METHOD OF RESTORING ENCAPSULATED INTEGRATED
CIRCUIT DEVICES

INFORMATION DISCLOSURE STATEMENT

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Assistant Commissioner for Patents
Washington, DC 20231

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Sir:

Enclosed herewith for filing in the above noted application is an Information Disclosure Statement identifying certain prior art resources of which the inventors have become aware. Copies of the identified documents are enclosed.

The two last named items could not be found in the publication but were obtained from Internet websites identified in the Information Disclosure Statement.

Respectfully submitted,

TIMOTHY CALVIN VISSER

Date:

6/24/02

By

Peter Visserman
Peter Visserman, Reg. No. 25,185

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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	Of	1
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Examiner Name	Not Assigned
Attorney Docket Number	Smiths P177US

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials ⁶	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the Item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		The Effects of Materials and Post-Mold Profiles on Plastic and Capsulated Integrated Circuits, IEEE/IRPS, 1994, pages 93-100	
		Select Topics in Assurance Related Technologies; Reliability Analysis Center; START Volume 2, No. 1, pages 1-6	
		Packaging of Electronics for High Temperature Environments, McCluskey et al. University of Maryland, pages 1-18	
		3.0 DEVICE CONSTRUCTION, Internet Website: http://epims.gsfc.nasa.gov/ctre/act/techdocs/pems/pem3.htm ; Goddard Light Center	
		4.0 GENERAL CHARACTERISTICS, Internet Website: http://epims.gsfc.nasa.gov/ctre/act/techdocs/pems/pem4.htm ; Goddard Flight Center	

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¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

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